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AMENDMENT TI	Docket SHM-0	Number 0901					
Application Number 09/450,351	Filing Date November 29, 199	 9	Examiner H. Tsai	Ar S S S S	t Unit 12		
Invention Title METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE						TEUN	
TO THE COMMISSIONER FOR PATENTS						7	JAN 22 2001
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Date: <u>January 12, 2001</u>

Donald W. Muirhead, Reg. No. 33,978

Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

(10-95)

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: YAMAMOTO

Appl. No.: 09/450,351

Filed: November 29, 1999

METHOD FOR MANUFACTURING A

SEMICONDUCTOR DEVICE

Art Unit: 2812

Examiner: TSAI, H.

Atty Docket: SHM-00901

CERTIFICATE OF MAILING

I hereby certify that the foregoing document is being deposited with the United States Postal Service as first class mail, postage prepaid, "Post Office to Addressee", in an envelope addressed to: Commissioner of Patents, Washington, DC 20231 on January 12, 2001.

AMENDMENT AND RESPONSE

Commissioner for Patents Washington, D.C. 20231

Sir:

For:

This paper is being provided in response to the Office Action dated November 1, 2000, for the above-captioned U.S. patent application.

It is not believed that extensions of time or fees for net addition of claims are required, beyond those which may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required for consideration of this paper (including fees for net addition of claims) are authorized to be charged in two originally-executed copies of an Amendment Transmittal Letter filed herewith.